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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE


Application No.: 10/725,318
Filed: 12/2/2003
Inventor(s):
Katsuki Hazama

Examiner: Booth, Richard A.
Group/Art Unit: 2812
Atty. Dkt. No: 5957-98500

Title: SEMICONDUCTOR DEVICE
INCLUDING TRANSISTOR
WITH COMPOSITE GATE
STRUCTURE AND
TRANSISTOR WITH SINGLE
GATE STRUCTURE, AND
METHOD FOR
MANUFACTURING THE
SAME

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Dean M. Munyon
Printed Name


Signature

February 21, 2006
Date

RESPONSE TO OFFICE ACTION
MAILED SEPTEMBER 21, 2005

M/S Amend
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

This paper is submitted in response to the Office Action of September 21, 2005.